

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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manapplication of: Dimpsey et al.

Serial No.: 10/806,633

Filed: March 22, 2004

For: Method and Apparatus for Providing Hardware Assistance for Data Access Coverage on Dynamically

Allocated Data

35525
PATENT TRADEMARK OFFICE

CUSTOMER NUMBER

Group Art Unit: 2188

Examiner: Padmanabhan, Mano

Attorney Docket No.: AUS920040062US1

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Respectfully submitted,

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Dimpsey et al.

Serial No: 10/806,633

Filed: March 22, 2004

Title: Method and Apparatus for Providing Hardware Assistance for Data Access Coverage on Dynamically Allocated Data

Group Art Unit: 2188

Intellectual Property Law Department International Business Machines Corporation 11400 Burnet Road Austin, Texas 78758

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. 1.56.

While this Supplemental Information Disclosure Statement may be "material" pursuant to 37 C.F.R. 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

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DOCKET NUMBER AUS920040062US1

Information Disclosure Statement.

No item of information cited in this Supplemental Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in this Supplemental Information Disclosure Statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of this Supplemental

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	Complete if Known
Application Number	10/806,633
Filing Date	03-22-2004
First Named Inventor	Dimpsey et al.
Examiner Name	Padmanabhan, Mano
Art Unit	2188
Attorney Docket No.	AUS920040062US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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Exam Init	-	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

TORRELLAS et al., "False Sharing and	Organization", 1984, Prentice-Hall, Inc., 2 nd Edition, pp. 10-12.
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Transactions on Computers, Vol. 43, N	
AJ ROTHMAN et al., "Analysis of Shared N	Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.

Examiner:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.